

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S3	173	cable near approxim\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/03 19:29
S4	14660	clock near (recaptur\$3 or recover\$3 or reus\$3 or reconstruct\$3 or restor\$4 )	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/28 08:12
S5	88696	PLL "phase-locked loop"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 14:18
S6	0	S3 & S4 & S5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 14:18
S7	0	S3 & S4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 14:18
S8	1	(cable near approxim\$4) near filter	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 14:24
S10	2	((("5,293,405") or ("5,933,467")).PN.	USPAT	OR	OFF	2007/06/27 17:06

## EAST Search History

S11	0	EP-0831480-\$.did. EP-0467412-\$. did. XP-002224302-\$.did.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 17:11
S13	10	((THOMAS) near2 (DUDA)).INV.	US-PGPUB; USPAT; USOCR	OR	ON	2007/06/27 19:21
S14	32	((LAJOS) near2 (GAZSI)).INV.	US-PGPUB; USPAT; USOCR	OR	ON	2007/06/27 19:35
S15	74	((PETER) near2 (GREGORIUS)).INV.	US-PGPUB; USPAT; USOCR	OR	ON	2007/06/27 19:36
S16	8	((TORSTEN) near2 (HINZ)).INV.	US-PGPUB; USPAT; USOCR	OR	ON	2007/06/27 19:36
S17	10	((MARTIN) near2 (RENNER)).INV.	US-PGPUB; USPAT; USOCR	OR	ON	2007/06/27 19:36
S18	120	S13 S14 S15 S16 S17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 19:37
S20	22	S18 & cable	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 19:38
S22	3	S18 & (cable near approximat\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/28 07:55
S23	1074	"375/355".CCLS.	US-PGPUB; USPAT; USOCR	OR	ON	2007/06/27 20:03

## EAST Search History

S24	14032	((375/316) or (375/229) or (375/232) or (375/350) or (375/345) or (375/376) or (360/46) or (360/25) or (360/65) or (360/67) or (369/59.21) or (369/59.22)). CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/27 20:08
S26	14914	S23 S24	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 20:09
S27	173	cable near approxim\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 20:35
S28	1	S26 & S27	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 20:10
S29	2	((("6130793") or ("5963525")).PN.	USPAT	OR	OFF	2007/07/04 14:21
S30	50	("4403252" "4955379" "6047032" "6366227" "6370191" "6522723" "7095803" "20030048858" "20040120433" "4476583" "4577226" "4951266" "5943646" "5459846" "5365229" "5555200" "5642405" "5644622" "5657374" "5852651" "5872809" "5889856" "6147713" "4852160" "4993063" "5504479" "5535150" "5577066" "5946355" "6025817" "6034801" "6052378" "4281408" "4362909" "4365249" RE31606 "4539675" "4796238" "5227870" "5371902" "5473321" "5479449" "5548617" "5570146" "5606579" "5617450" "5629641" "5659372" "5742622" "5754941" ).pn.	US-PGPUB; USPAT	OR	ON	2007/06/27 20:34

## EAST Search History

S31	0	S27 & S30	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/27 20:35
S32	14672	clock near (recaptur\$3 or recover\$3 or reus\$3 or reconstruct\$3 or restor\$4 )	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/28 08:12
S33	88757	PLL "phase-locked loop"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/28 08:12
S34	5416	S32 & S33	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/28 10:25
S35	158	S32 & S33 & decimat\$4 & interpolat\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/28 08:14
S36	1078	"375/355".CCLS.	US-PGPUB; USPAT; USOCR	OR	ON	2007/06/28 08:14
S37	11	S36 & S35	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/28 08:14

## EAST Search History

S38	14672	clock near (recaptur\$3 or recover\$3 or reus\$3 or reconstruct\$3 or restor\$4 )	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/28 10:28
S39	88757	PLL "phase-locked loop"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/28 10:28
S40	158	S38 & S39 & decimat\$4 & interpolat\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/28 10:28
S41	4	S40 & (interpolat\$4 near5 equaliz\$4 near3 signal) & (decimat\$4 near3 interpolatar\$4 near "5" PLL)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/28 10:28
S42	2	("5350885").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/03 19:29
S43	1	"10714202".rlan. or ("10".src. and "714202".ap.)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2007/07/04 09:09
S50	407	(filter\$4 near low-pass) near3 analog-to-digital	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/04 14:26

## EAST Search History

S51	259	((filter\$4 near low-pass) near2 analog-to-digital)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/04 14:26
S52	79	((filter\$4 near low-pass) near analog-to-digital)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/04 14:27
S53	0	((filter\$4 near low-pass) near analog-to-digital) near equaliz\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/04 14:27
S54	0	((filter\$4 near low-pass) near analog-to-digital) near3 equaliz\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/04 14:27
S55	2	((filter\$4 near low-pass) near analog-to-digital) near5 equaliz\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/04 14:27

Day : Wednesday

Date: 7/4/2007

Time: 16:20:26

PALM INTRANET

**Inventor Name Search Result**

Your Search was:

Last Name = DUDA

First Name = THOMAS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09936595</a>	Not Issued	161	01/14/2002	Method and device for coating a support body with a hard magnetic se-fe-b material using plasma spraying	DUDA, THOMAS
<a href="#">10429188</a>	<a href="#">6836188</a>	150	05/02/2003	PLL CIRCUIT AND METHOD FOR ELIMINATING SELF-JITTER IN A SIGNAL WHICH IS RECEIVED BY A CONTROL CIRCUIT	DUDA, THOMAS
<a href="#">10477137</a>	<a href="#">7194045</a>	150	11/07/2003	METHOD FOR DETERMINING A REFERENCE CLOCK PHASE FORM BAND-LIMITED DIGITAL DATA STREAMS	DUDA, THOMAS
<a href="#">10684528</a>	Not Issued	161	10/15/2003	Method of depositing an oxidation and fatigue resistant MCrAlY-coating	DUDA, THOMAS
<a href="#">10714202</a>	Not Issued	30	11/14/2003	Method for reconstructing data clocked at a symbol rate from a distorted analog signal	DUDA, THOMAS
<a href="#">10726545</a>	Not Issued	41	12/04/2003	Method of depositing a local MCrAlY-coating	DUDA, THOMAS
<a href="#">10726593</a>	Not Issued	41	12/04/2003	Method of depositing a local MCrAlY-coating	DUDA, THOMAS
<a href="#">10875722</a>	Not Issued	161	06/25/2004	Method of depositing a coating system	DUDA, THOMAS
<a href="#">10916776</a>	Not Issued	30	08/12/2004	Device for the production of standard-compliant signals	DUDA, THOMAS
<a href="#">11553748</a>	Not Issued	25	10/27/2006	Method for the Application of a Protective Coating to a Thermally Stressed Component	DUDA, THOMAS
<a href="#">11675191</a>	Not Issued	25	02/15/2007	METHOD FOR SYNCHRONIZING A CLOCK SIGNAL WITH A REFERENCE	DUDA, THOMAS

				SIGNAL, AND PHASE LOCKED LOOP	
<u>11687784</u>	Not Issued	17	03/19/2007	MEASURING DEVICE FOR MEASURING THE TEMPERATURE OF A THERMALLY LOADED METALLIC BASE ELEMENT, PROVIDED WITH A PROTECTIVE SURFACE COATING, AND METHOD FOR PRODUCING SUCH A MEASURING DEVICE	DUDA, THOMAS
<u>60892967</u>	Not Issued	20	03/05/2007	Organic product to repel mosquitoes	DUDA, THOMAS EUGENE
<u>10118742</u>	Not Issued	161	04/08/2002	Process for reclaiming mirror cullet and production of a powdered glass	DUDAK, THOMAS
<u>60092832</u>	Not Issued	159	07/14/1998	PROCESS FOR RECLAIMING MIRROR CULLET AND PRODUCTION OF A POWERED GLASS	DUDAK, THOMAS
<u>60282938</u>	Not Issued	159	04/10/2001	Process for reclaiming mirror cullet and production of a powdered glass	DUDAK, THOMAS
<u>09607061</u>	Not Issued	161	06/29/2000	Syringe for transfer of medication from a prefilled medication container	DUDAR, THOMAS E.
<u>09903134</u>	<u>6669681</u>	150	07/11/2001	NEEDLELESS CONNECTOR	DUDAR, THOMAS E.
<u>09907234</u>	<u>6569125</u>	150	07/17/2001	PRE-SLIT INJECTION SITE AND TAPERED CANNULA	DUDAR, THOMAS E.
<u>10730772</u>	Not Issued	30	12/08/2003	Needleless connector	DUDAR, THOMAS E.
<u>06585480</u>	Not Issued	166	03/02/1984	INJECTION SITE PACKAGE	DUDAR, THOMAS E.
<u>06854685</u>	<u>4698061</u>	150	04/18/1986	INJECTION SITE PACKAGE	DUDAR, THOMAS E.
<u>06868825</u>	Not Issued	166	05/29/1986	DRUG DELIVERY APPARATUS HAVING A PISTON-LIKE INJECTION SITE	DUDAR, THOMAS E.
<u>07114814</u>	<u>4850978</u>	150	10/29/1987	DRUG DELIVERY CARTRIDGE WITH PROTECTIVE COVER	DUDAR, THOMAS E.
<u>07119207</u>	Not	161	11/05/1987	DRUG DELIVERY	DUDAR, THOMAS



	Issued			APPARATUS HAVING A PISTON-LIKE INJECTION SITE	E.
<u>07147414</u>	Not Issued	168	01/25/1988	PRE-SLIT INJECTION SITE AND ASSOCIATED CANNULA	DUDAR, THOMAS E.
<u>07193457</u>	<u>5195992</u>	150	05/13/1988	PROTECTOR SHIELD FOR NEEDLES	DUDAR, THOMAS E.
<u>07217004</u>	Not Issued	166	07/08/1988	PRE-SLIT INJECTION SITE AND TAPERED CANNULA	DUDAR, THOMAS E.
<u>07320244</u>	<u>D321250</u>	150	03/07/1989	CANNULA	DUDAR, THOMAS E.
<u>07320245</u>	<u>D321251</u>	150	03/07/1989	CANNULA RECEIVER	DUDAR, THOMAS E.
<u>07320246</u>	<u>D321252</u>	150	03/07/1989	LOCKING CANNULA	DUDAR, THOMAS E.
<u>07325617</u>	Not Issued	166	03/17/1989	PRE-SLIT INJECTION SITE AND TAPERED CANNULA	DUDAR, THOMAS E.
<u>07335927</u>	<u>5135489</u>	150	04/10/1989	PRE-SLIT INJECTION SITE AND TAPERED CANNULA	DUDAR, THOMAS E.
<u>07419851</u>	<u>D327318</u>	150	10/10/1989	LOCKING CANNULA	DUDAR, THOMAS E.
<u>07425790</u>	<u>5100394</u>	150	10/23/1989	PRE-SLIT INJECTION SITE	DUDAR, THOMAS E.
<u>07435638</u>	Not Issued	166	11/09/1989	BAYONET LOCK CANNULA FOR PRE-SLIT Y-SITE	DUDAR, THOMAS E.
<u>07535935</u>	<u>5167648</u>	250	06/11/1990	PRE-SLIT INJECTION SITE AND ASSOCIATED CANNULA	DUDAR, THOMAS E.
<u>07536823</u>	<u>5171234</u>	250	06/12/1990	METHOD OF EFFECTING A TRANSFER OF FLUID FROM A SOURCE TO A RECEIVER	DUDAR, THOMAS E.
<u>07537395</u>	<u>5158554</u>	250	06/12/1990	PRE-SLIT INJECTION SITE AND ASSOCIATED CANNULA	DUDAR, THOMAS E.
<u>07539278</u>	<u>5188620</u>	250	06/14/1990	PRE-SLIT INJECTION SITE AND ASSOCIATED CANNULA	DUDAR, THOMAS E.
<u>07639773</u>	Not Issued	166	01/10/1991	PRESLIT INJECTION SITE AND TAPERED CANNULA	DUDAR, THOMAS E.
<u>07737735</u>	<u>5411499</u>	150	07/30/1991	NEEDLELESS VIAL ACCESS DEVICE	DUDAR, THOMAS E.
<u>07823498</u>	<u>5211638</u>	150	01/21/1992	PRE-SLIT INJECTION SITE	DUDAR, THOMAS E.

<a href="#">07841957</a>	<a href="#">5304130</a>	250	02/26/1992	CONTAINER FOR THE CONTROLLED ADMINISTRATION OF A BENEFICIAL AGENT	DUDAR, THOMAS E.
<a href="#">07909546</a>	<a href="#">5389086</a>	150	07/06/1992	SAFETY CANNULA	DUDAR, THOMAS E.
<a href="#">07926479</a>	Not Issued	166	08/07/1992	NEEDLELESS ACCESS STOPPER	DUDAR, THOMAS E.
<a href="#">08021644</a>	Not Issued	166	02/22/1993	BAYONET LOCK CANNULA FOR PRE-SLIT Y-SITE	DUDAR, THOMAS E.
<a href="#">08149590</a>	Not Issued	166	11/09/1993	PRE-SLIT INJECTION SITE AND TAPERED CANNULA FOR FLUID SAMPLING	DUDAR, THOMAS E.
<a href="#">08183110</a>	Not Issued	166	01/18/1994	PRE-SLIT INJECTION SITE AND TAPERED CANNULA	DUDAR, THOMAS E.
<a href="#">08234758</a>	Not Issued	166	04/28/1994	PRE-SLIT INJECTION SITE AND TAPERED CANNULA	DUDAR, THOMAS E.

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	<b>Last Name</b>	<b>First Name</b>	
<b>Search Another: Inventor</b>	<input type="text" value="DUDA"/>	<input type="text" value="THOMAS"/>	<input type="button" value="Search"/>


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Day : Wednesday

Date: 7/4/2007

Time: 16:20:38

 PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = GAZSI

First Name = LAJOS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">06698854</a>	<a href="#">4706188</a>	150	02/06/1985	METHOD AND APPARATUS FOR READING SAMPLES OF A TIME-DEPENDENT SIGNAL IN A DATA PROCESSING SYSTEM	GAZSI, LAJOS
<a href="#">07014258</a>	<a href="#">4825396</a>	150	02/12/1987	DIGITAL CIRCUIT FOR SAMPLING RATE VARIATION AND SIGNAL FILTERING AND METHOD FOR CONSTRUCTING THE CIRCUIT	GAZSI, LAJOS
<a href="#">07532435</a>	<a href="#">5138568</a>	150	06/04/1990	METHOD AND CIRCUIT CONFIGURATION FOR AVOIDING OVERFLOWS IN AN ADAPTIVE, RECURSIVE DIGITAL WAVE FILTER HAVING FIXED POINT ARITHMETIC	GAZSI, LAJOS
<a href="#">07538821</a>	Not Issued	168	06/15/1990	METHOD AND NETWORK CONFIGURATION FOR ATTAINING A CONTINUOUS VARIATION IN THE TRANSFER FUNCTION OF AN ADAPTIVE RECURSIVE NETWORK FOR PROCESSING DISCRETE-TIME SIGNALS	GAZSI, LAJOS
<a href="#">07538842</a>	<a href="#">5130943</a>	150	06/15/1990	METHOD AND CONFIGURATION FOR IMPROVING THE DYNAMIC RANGE OF AN ADAPTIVE RECURSIVE NETWORK FOR PROCESSING DISCRETE-TIME SIGNALS	GAZSI, LAJOS
<a href="#">07625207</a>	<a href="#">5075679</a>	150	12/10/1990	ANALOG/DIGITAL CONVERTER CONFIGURATION WITH SIGMA-DELTA MODULATORS	GAZSI, LAJOS

<u>07675314</u>	Not Issued	161	03/26/1991	ADAPTIVE RECURSIVE DIGITAL FILTER	GAZSI, LAJOS
<u>07676193</u>	<u>5172411</u>	150	03/27/1991	TWO-WIRE/FOUR-WIRE CONVERTER	GAZSI, LAJOS
<u>07676197</u>	<u>5175763</u>	150	03/27/1991	TWO-WIRE/FOUR-WIRE CONVERTER	GAZSI, LAJOS
<u>07805584</u>	<u>5204831</u>	150	12/11/1991	CIRCUIT CONFIGURATION FOR DIGITAL BIT-SERIAL SIGNAL PROCESSING	GAZSI, LAJOS
<u>07807167</u>	<u>5233550</u>	150	12/13/1991	ADAPTIVE BALANCE FILTER	GAZSI, LAJOS
<u>07888473</u>	<u>5233548</u>	150	05/22/1992	METHOD AND NETWORK CONFIGURATION FOR ATTAINING A CONTINUOUS VARIATION IN THE TRANSFER FUNCTION OF AN ADAPTIVE RECURSIVE NETWORK FOR PROCESSING DISCRETE-TIME SIGNALS	GAZSI, LAJOS
<u>07947724</u>	<u>5251162</u>	150	09/18/1992	INTEGRATABLE CIRCUIT CONFIGURATION WITH AN ANALOG NETWORK	GAZSI, LAJOS
<u>07947725</u>	<u>5243548</u>	150	09/18/1992	INTEGRATABLE CIRCUIT CONFIGURATION WITH AN ANALOG NETWORK	GAZSI, LAJOS
<u>08014839</u>	<u>5315621</u>	150	02/08/1993	ADAPTIVE NONRECURSIVE DIGITAL FILTER AND METHOD FOR FORMING FILTER COEFFICIENTS THEREFOR	GAZSI, LAJOS
<u>08093382</u>	Not Issued	161	07/19/1993	ADAPTIVE RECURSIVE DIGITAL FILTER	GAZSI, LAJOS
<u>08392074</u>	<u>5500613</u>	150	02/22/1995	METHOD FOR PRODUCING A DIGITAL SINE WAVE SIGNAL WITH A GIVEN SAMPLING RATE, AND CIRCUIT CONFIGURATION FOR CARRYING OUT THE METHOD	GAZSI, LAJOS
<u>08404366</u>	<u>5629881</u>	150	03/15/1995	METHOD FOR FILTERING A DIGITAL VALUE TRAIN WITH IMPROVED NOISE BEHAVIOR, AND CIRCUIT CONFIGURATION FOR PERFORMING THE METHOD	GAZSI, LAJOS
<u>08535714</u>	<u>5805639</u>	150	09/28/1995	ADAPTIVE NETWORK	GAZSI, LAJOS
<u>09281101</u>	Not	161	03/29/1999	CONFIGURATION FOR	GAZSI, LAJOS

	Issued			CONNECTING A NUMBER OF SUBSCRIBER LINES TO A TELECOMMUNICATIONS SYSTEM	
<u>09357249</u>	<u>6594360</u>	150	07/20/1999	CIRCUIT CONFIGURATION FOR TWO-WIRE/FOUR-WIRE CONVERSION	GAZSI, LAJOS
<u>09797244</u>	<u>6870879</u>	150	03/01/2001	INTERPOLATION FILTER CIRCUIT	GAZSI, LAJOS
<u>09803384</u>	<u>7079538</u>	150	03/09/2001	HIGH-SPEED ROUTER	GAZSI, LAJOS
<u>10019366</u>	<u>6829629</u>	150	06/12/2002	COMB FILTER SYSTEM FOR DECIMATING A SEQUENCE OF DIGITAL INPUT VALUES TO A SEQUENCE OF DIGITAL OUTPUT VALUES BY A NON-INTEGGER FACTOR	GAZSI, LAJOS
<u>10080414</u>	<u>6563867</u>	150	02/22/2002	ARRANGEMENT FOR ANALYZING THE NONLINEAR PROPERTIES OF A COMMUNICATION CHANNEL	GAZSI, LAJOS
<u>10225637</u>	<u>7095803</u>	150	08/22/2002	METHOD OF RECONSTRUCTING DATA TRANSMITTED OVER A TRANSMISSION PATH IN A RECEIVER AND CORRESPONDING DEVICE	GAZSI, LAJOS
<u>10484959</u>	Not Issued	41	01/27/2004	Device for correcting a receiving signal	GAZSI, LAJOS
<u>10714202</u>	Not Issued	30	11/14/2003	Method for reconstructing data clocked at a symbol rate from a distorted analog signal	GAZSI, LAJOS
<u>10916776</u>	Not Issued	30	08/12/2004	Device for the production of standard-compliant signals	GAZSI, LAJOS
<u>10987935</u>	Not Issued	41	11/12/2004	Parallel multithread processor (PMT) with split contexts	GAZSI, LAJOS
<u>11064795</u>	Not Issued	93	02/24/2005	HETEROGENEOUS PARALLEL MULTITHREAD PROCESSOR (HPMT) WITH LOCAL CONTEXT MEMORY SETS FOR RESPECTIVE PROCESSOR TYPE GROUPS AND GLOBAL CONTEXT MEMORY	GAZSI, LAJOS
<u>11501408</u>	Not Issued	30	08/09/2006	Digital phase locked loop for sub-mu technologies	GAZSI, LAJOS
<u>60115850</u>	Not	159	01/13/1999	Speed-up of viterbi decoding by	GAZSI, LAJOS

	Issued		means of parallelism	
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**Inventor Search Completed:** No Records to Display.

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	Last Name	First Name	
<b>Search Another: Inventor</b>	<input type="text" value="GAZSI"/>	<input type="text" value="LAJOS"/>	<input type="button" value="Search"/>

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PALM INTRANET

**Inventor Name Search Result**

Your Search was:

Last Name = GREGORIUS

First Name = PETER

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10090318</a>	<a href="#">6710603</a>	150	03/04/2002	OVERLOAD PROTECTION CIRCUIT FOR LINE DRIVERS	GREGORIUS, PETER
<a href="#">10131375</a>	<a href="#">6700361</a>	150	04/24/2002	VOLTAGE REGULATOR WITH A STABILIZATION CIRCUIT FOR GUARANTEEING STABLE OPERATION.	GREGORIUS, PETER
<a href="#">10219275</a>	<a href="#">7173993</a>	150	08/16/2002	METHOD FOR SAMPLING PHASE CONTROL	GREGORIUS, PETER
<a href="#">10225637</a>	<a href="#">7095803</a>	150	08/22/2002	METHOD OF RECONSTRUCTING DATA TRANSMITTED OVER A TRANSMISSION PATH IN A RECEIVER AND CORRESPONDING DEVICE	GREGORIUS, PETER
<a href="#">10229867</a>	<a href="#">7106812</a>	150	08/28/2002	DEVICE FOR THE RECOVERY OF DATA FROM A RECEIVED DATA SIGNAL	GREGORIUS, PETER
<a href="#">10301444</a>	<a href="#">7184502</a>	150	11/21/2002	CIRCUIT ARRANGEMENT FOR RECOVERING CLOCK AND DATA FROM A RECEIVED SIGNAL	GREGORIUS, PETER
<a href="#">10407033</a>	Not Issued	80	04/03/2003	Method and apparatus for phase detection	GREGORIUS, PETER
<a href="#">10478452</a>	<a href="#">7209004</a>	150	06/25/2004	DB-LINEAR VARIABLE GAIN AMPLIFIER (VGA) STAGE WITH A HIGH BROAD BAND	GREGORIUS, PETER
<a href="#">10481856</a>	Not Issued	71	12/22/2003	Transmitter for transmitting digital data over a transmission line	GREGORIUS, PETER
<a href="#">10485336</a>	<a href="#">7212038</a>	150	09/16/2004	LINE DRIVER FOR TRANSMITTING DATA	GREGORIUS, PETER

<u>10492390</u>	<u>7088976</u>	150	10/18/2004	DEVICE FOR RECONSTRUCTING DATA FROM A RECEIVED DATA SIGNAL AND CORRESPONDING TRANSCEIVER	GREGORIUS, PETER
<u>10714202</u>	Not Issued	30	11/14/2003	Method for reconstructing data clocked at a symbol rate from a distorted analog signal	GREGORIUS, PETER
<u>10714811</u>	Not Issued	93	11/17/2003	METHOD AND FILTER ARRANGEMENT FOR DIGITAL RECURSIVE FILTERING IN THE TIME DOMAIN	GREGORIUS, PETER
<u>10737728</u>	<u>6911930</u>	150	12/15/2003	CELL ARRAY WITH MISMATCH REDUCTION	GREGORIUS, PETER
<u>10755696</u>	<u>7127061</u>	150	01/12/2004	LINE DRIVER FOR DIGITAL SIGNAL TRANSMISSION	GREGORIUS, PETER
<u>10808143</u>	<u>7154809</u>	150	03/24/2004	METHOD FOR MEASURING THE DELAY TIME OF A SIGNAL LINE	GREGORIUS, PETER
<u>10808167</u>	Not Issued	90	03/24/2004	FEED FORWARD EQUALIZER AND A METHOD FOR ANALOG EQUALIZATION OF A DATA SIGNAL	GREGORIUS, PETER
<u>10809122</u>	Not Issued	30	03/25/2004	Clock and data recovery unit	GREGORIUS, PETER
<u>10810981</u>	Not Issued	93	03/26/2004	FEED FORWARD CLOCK AND DATA RECOVERY UNIT	GREGORIUS, PETER
<u>10916776</u>	Not Issued	30	08/12/2004	Device for the production of standard-compliant signals	GREGORIUS, PETER
<u>10954869</u>	<u>7180821</u>	150	09/30/2004	MEMORY DEVICE; MEMORY CONTROLLER AND MEMORY SYSTEM HAVING BIDIRECTIONAL CLOCK LINES	GREGORIUS, PETER
<u>10955177</u>	<u>7173877</u>	150	09/30/2004	MEMORY SYSTEM WITH TWO CLOCK LINES AND A MEMORY DEVICE	GREGORIUS, PETER
<u>10970516</u>	Not Issued	30	10/21/2004	Method and device for estimating channel properties of a transmission channel	GREGORIUS, PETER
<u>10986767</u>	<u>7050340</u>	150	11/15/2004	SEMICONDUCTOR MEMORY SYSTEM AND METHOD FOR THE TRANSFER OF WRITE	GREGORIUS, PETER



				AND READ DATA SIGNALS IN A SEMICONDUCTOR MEMORY SYSTEM	
<u>10996956</u>	Not Issued	71	11/24/2004	Integrated memory device and memory module	GREGORIUS, PETER
<u>11002148</u>	Not Issued	41	12/03/2004	Semiconductor memory module	GREGORIUS, PETER
<u>11046160</u>	<u>7215597</u>	150	01/28/2005	MEMORY DEVICE HAVING COMPONENTS FOR TRANSMITTING AND RECEIVING SIGNALS SYNCHRONOUSLY	GREGORIUS, PETER
<u>11128789</u>	Not Issued	71	05/13/2005	Memory system and method of accessing memory chips of a memory system	GREGORIUS, PETER
<u>11152769</u>	<u>7184360</u>	150	06/15/2005	HIGH-SPEED INTERFACE CIRCUIT FOR SEMICONDUCTOR MEMORY CHIPS AND MEMORY SYSTEM INCLUDING SEMICONDUCTOR MEMORY CHIPS	GREGORIUS, PETER
<u>11170887</u>	Not Issued	30	06/30/2005	Synchronous signal generator	GREGORIUS, PETER
<u>11171110</u>	Not Issued	30	06/30/2005	Semiconductor memory system	GREGORIUS, PETER
<u>11171585</u>	Not Issued	30	06/30/2005	Semiconductor memory chip and method of protecting a memory core thereof	GREGORIUS, PETER
<u>11174950</u>	Not Issued	41	07/05/2005	Memory controller with a plurality of parallel transfer blocks	GREGORIUS, PETER
<u>11192335</u>	Not Issued	41	07/29/2005	Semiconductor memory module and system	GREGORIUS, PETER
<u>11193184</u>	Not Issued	30	07/29/2005	Semiconductor memory chip and memory system	GREGORIUS, PETER
<u>11194509</u>	Not Issued	41	08/01/2005	Device for setting a clock delay	GREGORIUS, PETER
<u>11194510</u>	Not Issued	41	08/01/2005	Device for the regulated delay of a clock signal	GREGORIUS, PETER
<u>11194770</u>	Not Issued	93	08/01/2005	METHOD AND DEVICE FOR GENERATING A CLOCK SIGNAL USING A PHASE DIFFERENCE SIGNAL AND A FEEDBACK SIGNAL	GREGORIUS, PETER

<a href="#">11194773</a>	Not Issued	41	08/01/2005	Method of operating a memory device, memory module, and a memory device comprising the memory module	GREGORIUS, PETER
<a href="#">11213550</a>	Not Issued	30	08/26/2005	Code driver for a memory controller	GREGORIUS, PETER
<a href="#">11226456</a>	Not Issued	71	09/15/2005	Semiconductor memory chip with re-drive unit for electrical signals	GREGORIUS, PETER
<a href="#">11226457</a>	Not Issued	30	09/15/2005	High-speed interface circuit for semiconductor memory chips and memory system including the same	GREGORIUS, PETER
<a href="#">11238331</a>	Not Issued	30	09/26/2005	Phase locked loop having reduced inherent noise	GREGORIUS, PETER
<a href="#">11239829</a>	Not Issued	30	09/30/2005	Semiconductor memory system and memory module	GREGORIUS, PETER
<a href="#">11242150</a>	<a href="#">7221615</a>	150	10/04/2005	SEMICONDUCTOR MEMORY CHIP	GREGORIUS, PETER
<a href="#">11259376</a>	Not Issued	30	10/26/2005	Method of transferring signals between a memory device and a memory controller	GREGORIUS, PETER
<a href="#">11261911</a>	Not Issued	30	10/31/2005	Semiconductor memory chip, semiconductor memory module and method for transmitting write data to semiconductor memory chips	GREGORIUS, PETER
<a href="#">11264059</a>	Not Issued	30	10/31/2005	Memory module and memory device and method of operating a memory device	GREGORIUS, PETER
<a href="#">11264060</a>	Not Issued	93	10/31/2005	GENERATING A SAMPLING CLOCK SIGNAL IN A COMMUNICATION BLOCK OF A MEMORY DEVICE	GREGORIUS, PETER
<a href="#">11267930</a>	Not Issued	30	11/04/2005	Timing recovery phase locked loop	GREGORIUS, PETER

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**Inventor Name Search Result**

Your Search was:

Last Name = HINZ

First Name = TORSTEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09619973</a>	<a href="#">6876710</a>	150	07/20/2000	DIGITALLY CONTROLLED CIRCUIT FOR REDUCING THE PHASE MODULATION OF A SIGNAL	HINZ, TORSTEN
<a href="#">10477137</a>	<a href="#">7194045</a>	150	11/07/2003	METHOD FOR DETERMINING A REFERENCE CLOCK PHASE FORM BAND-LIMITED DIGITAL DATA STREAMS	HINZ, TORSTEN
<a href="#">10492390</a>	<a href="#">7088976</a>	150	10/18/2004	DEVICE FOR RECONSTRUCTING DATA FROM A RECEIVED DATA SIGNAL AND CORRESPONDING TRANSCEIVER	HINZ, TORSTEN
<a href="#">10714202</a>	Not Issued	30	11/14/2003	Method for reconstructing data clocked at a symbol rate from a distorted analog signal	HINZ, TORSTEN
<a href="#">11360988</a>	Not Issued	41	02/23/2006	DLL circuit for providing an adjustable phase relationship with respect to a periodic input signal	HINZ, TORSTEN
<a href="#">11464999</a>	Not Issued	25	08/16/2006	PHASE SHIFTER	HINZ, TORSTEN
<a href="#">11740762</a>	Not Issued	19	04/26/2007	MEMORY-MODULE CONTROLLER, MEMORY CONTROLLER AND CORRESPONDING MEMORY ARRANGEMENT, AND ALSO METHOD FOR ERROR CORRECTION	HINZ, TORSTEN

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Your Search was:

Last Name = RENNER

First Name = MARTIN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">07111935</a>	<a href="#">4881229</a>	150	10/21/1987	TEST CIRCUIT ARRANGEMENT FOR A COMMUNICATIONS NETWORK AND TEST METHOD USING SAME	RENNER, MARTIN
<a href="#">07616875</a>	<a href="#">5084685</a>	150	11/21/1990	MICROCOMPUTER HAVING AN INTERGRATED RC OSCILLATOR WITH PROGRAMMABLE FREQUENCY	RENNER, MARTIN
<a href="#">09771289</a>	<a href="#">6551239</a>	150	01/26/2001	SHAFT FOR A FLEXIBLE ENDOSCOPE AND FLEXIBLE ENDOSCOPE	RENNER, MARTIN
<a href="#">09988955</a>	<a href="#">6676598</a>	150	11/21/2001	LARYNGOSCOPE	RENNER, MARTIN
<a href="#">10477137</a>	<a href="#">7194045</a>	150	11/07/2003	METHOD FOR DETERMINING A REFERENCE CLOCK PHASE FORM BAND-LIMITED DIGITAL DATA STREAMS	RENNER, MARTIN
<a href="#">10539774</a>	Not Issued	20	05/23/2006	Gripper and method of operating the same	RENNER, MARTIN
<a href="#">10714202</a>	Not Issued	30	11/14/2003	Method for reconstructing data clocked at a symbol rate from a distorted analog signal	RENNER, MARTIN
<a href="#">11752486</a>	Not Issued	19	05/23/2007	Endoscope, In Particular For The Intubation Of A Respiratory Tract	RENNER, MARTIN
<a href="#">60797055</a>	Not Issued	159	05/02/2006	Production of sodium alkylisethionate isomeric mixtures and corresponding alkylisethionate esters	RENNER, MARTIN A.

**Inventor Search Completed: No Records to Display.**

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# Decimation (signal processing)

From Wikipedia, the free encyclopedia

*This article is related to signal processing. For other meanings of the word **Decimation**, please see Decimation (disambiguation).*

In digital signal processing, **decimation** is a technique for reducing the number of samples in a discrete-time signal.

Decimation is a two-step process:

1. Low-pass anti-aliasing filter
2. Downsampling

An example of decimation: the frequency of a recorded sound can be raised an octave (in other words, doubled in frequency) by eliminating every other sample without changing the sampling rate. This will result in aliasing if the sound contains overtones whose (doubled) frequency will exceed half the sampling rate. Decimation aliasing can be avoided by eliminating those overtones.

The same principle applies to eliminating samples at other intervals.

## See also

- Digital signal processing
- Signal processing

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